

WORK EXPERIENCE:

- Assistant Professor, Department of Electronics and Computer Engineering September 2013 – Present
(National Institute of Technology, Arunachal Pradesh, Yupia)
- Assistant Professor, Department of Electronics and Communication Engineering June 2013 – July 2013
(Bengal College of Engineering and Technology, Durgapur)
- Research Scholar January 2012 – April 2013
(Malaviya National Institute of Technology, Jaipur)
Designed a reconfigurable router using HDL for applications in NoC. The performance of the router was checked in Xilinx for different routing algorithms. The goal of the work was to reduce the bit traversal time by minimizing routing computation time without having extra area overhead.

SUBJECTS TAUGHT:

- Digital Electronics and Logic Design (ECE 201)
- Signals and Systems (ECE 402)
- Digital Signal Processing (ECE 504)
- VLSI Design (ECE 703)
- CMOS Digital IC Design (ECE 955/ECE 903)

LABS TAUGHT:

- Digital Electronics and Logic Design
- VLSI Design

PROGRAMMING LANGUAGE AND TOOLS

- Verilog
- Xilinx
- Lt Spice
- Cadence

ON GOING PROJECTS:

- Design and analysis of transceiver circuit for bidirectional and simultaneous bidirectional signaling
- Use of optimization techniques in the design and modeling of MOS circuit optimization
- Design of process aware ring oscillator and delay circuit for generation of variable duty cycle

AREAS OF INTEREST:

- Transceiver circuit for high frequency applications
- Analog circuit
- Networks on Chip (NoC)
- Current mode logic

EDUCATION:

- PhD in Electronics and Communication Engineering November 2018
(National Institute of Technology Arunachal Pradesh, Yupia, India)
- M.Tech in Microelectronics and VLSI June 2011
(National Institute of Technology Durgapur, West Bengal, India)
- B.E in Electronics and Communication Engineering June 2007
(University Institute of Technology, Burdwan University, West Bengal, India)